



SOI INDUSTRY CONSORTIUM REINFORCES ITS ACADEMIC AND RESEARCH SUPPORT; THREE MORE PRESTIGIOUS UNIVERSITIES JOIN

BOSTON, MASS., June 17, 2009 – The SOI Industry Consortium, an international group aimed at accelerating broad adoption of silicon-on-insulator (SOI) materials technology across semiconductor markets, announced today that it is reinforcing its academic and research support with the additions of three prestigious universities at the forefront of SOI applied research. Stanford University, University of California, Berkeley and Ritsumeikan University, Kyoto, Japan have all joined the consortium as academic members.

“We are very pleased to welcome Stanford, U.C. Berkeley and Ritsumeikan University. With these additions, we are expanding our excellent ecosystem of prestigious academic and research support,” says Horacio Mendez, Executive Director of the SOI Industry Consortium. “Our members in academia are key contributors in both basic and applied SOI research. Together, we can facilitate cooperation with industry and other research organizations, helping to accelerate the use of SOI in both emerging and established application fields. Conversely, academia is our gateway to the future, providing our industry with researchers and engineers who have a strong grounding in the use and advantages of SOI.”

The SOI Industry Consortium will continue promoting cooperation between commercial members and academia and research institutes. Representing leaders spanning the entire electronics industry infrastructure, the SOI Industry Consortium’s membership now counts seven academic members and research institutes worldwide. The others include Tyndall Institute, “Université Catholique de Louvain” (UCL), as well as IMEC, and Leti. The group’s expertise covers key areas such as ultra-low power applications, MEMS, advanced CMOS, Fully Depleted (FD) SOI, FinFETS, photonics, analog, SOC and RF applications.

The SOI Industry Consortium is open to any company, organization or academic institution with an interest in SOI.

About the SOI Industry Consortium:

The SOI Industry Consortium is chartered with accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption. Representing leaders spanning the entire electronics industry infrastructure, SOI Industry Consortium members include today: Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, Globalfoundries, IBM, IMEC, Infotech, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Nvidia, Ritsumeikan University, Samsung, Semico, Soitec, SEH Europe, Stanford University, STMicroelectronics, Symmid Semiconductor, Synopsys, TSMC, Tyndall Institute, UC Berkeley, University Catholique de Louvain, and UMC. Membership is open to all companies and institutions throughout the electronics industry. For more information, see www.soiconsortium.org

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