



IMEC JOINS THE SOI INDUSTRY CONSORTIUM

BOSTON, MASS., February 23, 2008 – The SOI Industry Consortium, aimed at accelerating silicon-on-insulator (SOI) innovation into broad markets, announced today that IMEC has joined the organization as an academic member. IMEC is a world-leading independent nanoelectronics research center headquartered in Leuven, Belgium. It has been active in the field of SOI technologies for more than two decades.

“SOI has long been one of the key routes on IMEC’s roadmaps. In semiconductor technology, platform creation is of vital importance in making progress, creating critical mass, and identifying common interests between various players. That is what IMEC is doing, and that is what the SOI Industry Consortium is doing. We support the activities of the consortium and look forward to moving forward together,” says Luc Van den hove, Chief Operating Officer and Executive Vice President of IMEC.

“We are extremely pleased to have IMEC as part of the SOI Consortium,” says Horacio Mendez, Executive Director of the SOI Industry Consortium. “IMEC is recognized as one of the world’s leading semiconductor R&D institutes. The expertise and knowledge they bring to this partnership will be exceptionally valuable in maximizing SOI’s capabilities.”

IMEC’s collaborative CMOS scaling research platform targets technology generations two to three nodes ahead of state-of-the-art IC production. The activities are organized as a cluster of programs enabling very advanced research. Based on its long-standing track record and expertise, IMEC’s central focus in these programs is on advanced process module and device research including the exploration of new materials. Beside fundamental studies related to device physics, IMEC is also processing both partially depleted (PD) and fully depleted (FD) SOI devices.

The SOI Industry Consortium is open to any company, organization or academic institution with an interest in SOI.

About the SOI Industry Consortium:

The SOI Industry Consortium is chartered with accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption. Representing leaders spanning the entire electronics industry infrastructure, SOI Industry Consortium members include today: AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, IMEC, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Nvidia, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Symmid Semiconductor, Synopsys, Time To Market, TSMC, Tyndall Institute, University Catholique de Louvain, and UMC. Membership is open to all companies and institutions throughout the electronics industry.

For more information, see www.soiconsortium.org

Legal Note

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About IMEC:

IMEC is a world-leading independent research center in nanoelectronics and nanotechnology. IMEC vzw is headquartered in Leuven, Belgium, has a sister company in the Netherlands, IMEC-NL, offices in the US, China and Taiwan, and representatives in Japan. Its staff of more than 1650 people includes about 600 industrial residents and guest researchers. In 2008, its revenue (P&L) was estimated to EUR 264 million. IMEC's More Moore research aims at semiconductor scaling towards sub-32nm nodes. With its More than Moore research, IMEC looks into technologies for nomadic embedded systems, wireless autonomous transducer solutions, biomedical electronics, photovoltaics, organic electronics and GaN power electronics.

IMEC's research bridges the gap between fundamental research at universities and technology development in industry. Its unique balance of processing and system know-how, intellectual property portfolio, state-of-the-art infrastructure and its strong network worldwide position IMEC as a key partner for shaping technologies for future systems.

Further information on IMEC can be found at www.imec.be

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