



## **MENTOR GRAPHICS TO JOIN THE SOI INDUSTRY CONSORTIUM**

### **Expands EDA Industry Support for Energy-Efficient SOI Technology**

**Boston, MA**, November 12, 2009 – The SOI Industry Consortium, aimed at accelerating silicon-on-insulator (SOI) innovation into broad markets, announced today that Mentor Graphics Corporation (Nasdaq: MENT) has joined the worldwide organization. Mentor is a global leader in electronic hardware and software design solutions. As an active member and a leader in design enablement, Mentor will expand EDA tool and methodology support for SOI technology. This engagement connects Mentor’s strengths in low-power solutions with the inherent energy-efficiency benefits of SOI technology.

“Mentor Graphics is pleased to join the SOI Industry Consortium, and looks forward to advancing design automation support for SOI-based products and systems in collaboration with other members,” said Robert Hum, VP and GM, Deep Submicron Technologies at Mentor Graphics. “As we engage our expertise in modeling, characterization, analytics and constructive design tools with the needs of the global SOI user community, we can help designers and manufacturers exploit all of the underlying goodness of SOI technology.”

“We welcome Mentor Graphics as our newest member, and are excited about the many prospects. Their EDA technology leadership in green, low-power design and design-for-manufacturing solutions will benefit many of our members,” said Horacio Mendez, executive director of the SOI Industry Consortium. “Mentor’s strengths in mixed signal and RF design enablement are also well-aligned with the expanding usage of SOI.”

The SOI Industry Consortium welcomes companies, organizations, government and academic institutions to join the group in applying the full benefits of SOI-based electronics to global sustainability challenges and lowering the total cost-of-ownership of electronics.

#### About the SOI Industry Consortium:

The SOI Industry Consortium is chartered with accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption. Representing innovation leaders from the entire electronics industry infrastructure, current SOI Industry Consortium members include: AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, GLOBALFOUNDRIES, IBM, IMEC, Infotech, Innovative Silicon, Kanazawa Institute of Technology, KLA-Tencor, Magma Design, Mentor Graphics, Nvidia, Ritsumeikan University, Samsung, Semico, SEH Europe, Soitec, Stanford University,

STMicroelectronics, Synopsys, TSMC, Tyndall Institute, University of California-Berkeley, University Catholique de Louvain, UMC and Varian. Membership is open to all companies and institutions throughout the electronics industry. For more information, please visit [www.soiconsortium.org](http://www.soiconsortium.org).

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