



NVIDIA JOINS THE SOI INDUSTRY CONSORTIUM

SEMICON WEST, San Francisco, CA, July 16th, 2008 – The SOI Industry Consortium announced today that NVIDIA (Nasdaq: NVDA), the world leader in visual computing technologies, has joined the organization. The SOI Industry Consortium, which was formed in October 2007 by a group of leading companies from across the electronics industry, is aimed at accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption.

NVIDIA serves the entertainment and consumer market, the professional design and visualization market, as well as the high-performance computing market. Their products are transforming visually-rich and computationally-intensive applications such as video games, film production, broadcasting, industrial design, financial modeling, space exploration, and medical imaging.

NVIDIA brings the SOI Consortium membership to twenty three companies. Other members include: AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Synopsys, TSMC, Tyndall Institute, UCL and UMC.

“NVIDIA is pleased to join the SOI consortium. We are looking forward in participating on the advancement of such an innovative technology and its applications to future products,” said John Chen, VP, Technology and Foundry Operations at NVIDIA.

“We are honored to partner with NVIDIA. Their unique insight and perspectives as the world leader in visual computing will be very valuable in articulating the technology direction of the consortium,” says Horacio Mendez, executive director of the SOI Industry Consortium.

“NVIDIA’s entry into the SOI Industry Consortium supports the momentum we are gaining throughout the electronics industry from end-users and enablers to suppliers and manufacturers,” added Mendez.

The SOI Industry Consortium is open to any company, organization or academic institution with an interest in SOI.

Visit the SOI Industry Consortium in San Francisco, at Semicon West, West Hall, level 2 booth #L12.

About the SOI Industry Consortium:

The SOI Industry Consortium is chartered with accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption. Representing leaders spanning the entire electronics industry infrastructure, SOI Industry Consortium members include: AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing Ltd, Freescale Semiconductor, IBM, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Synopsys, TSMC , Tyndall Institute, UCL and UMC. Membership is open to all companies and institutions throughout the electronics industry. For more information, see www.soiconsortium.org

Legal note

The views and opinions expressed by the SOI Industry Consortium through officers in the SOI Industry Consortium or in this presentation or other communication vehicles are not necessarily representative of the views and opinions of individual members. Officers of the SOI Industry Consortium speaking on behalf of the Consortium should not be considered to be speaking for the member company or companies they are associated with, but rather as representing the views of the SOI Industry Consortium. Views and opinions are also subject to change without notice, and the SOI Industry Consortium assumes no obligation to update the information in this communication or accompanying discussions.

Press Contact: (also at Semicon West for interviews at the show)

Camille Darnaud-Dufour

Phone: +33 (0) 6 9 49 51 43

Email : camille.darnaud-dufour@soiconsortium.org

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