

FD SOI ARCHITECTURE, TECHNOLOGY PLATFORM FOR LOW POWER APPLICATIONS FOR 22nm AND BEYOND

When/Where: Friday Oct 16, Leuven, Belgium

9:00-17:00 - WORKSHOP SESSIONS - IMEC auditorium

17:00-20:00 - NETWORKING RECEPTION - IMEC entrance hall

Agenda	Speaker	Topic	Affiliation
9:00 - 9:10	Luc Van den hove	Opening remarks	IMEC
9:10 - 9:40	Ghavam Shahidi	Fully Depleted SOI Technology for 22 nm and beyond	IBM
9:40 - 10:10	Nobuyuki Sugii	Hybrid SOI and bulk integration	Hitachi
10:10 - 10:40	Daniel Delprat	UTSOI and UTBOX wafer readiness	Soitec
10:40 - 11:00	coffee break		
11:00 - 11:30	Tsu Jae King	SRAM yield enhancement with FDSOI	UCB
11:30 - 12:00	Pierre Fazan	ZRAM for embedded and stand alone applications	ISi
12:00 - 12:30	Horacio Mendez	FinFET case study: SOI vs. bulk	SOI Consortium
12:30 - 14:00	Lunch		
14:00 - 14:30	Olivier Faynot	Planar FDSOI technology: performance and perspectives	CEA-LETI
14:30 - 15:00	Bora Nikolic	Porting designs to FDSOI	UCB
15:00 - 15:30	ARM Representative	ARM 1176 implementation in SOI 45 nm and outlook	ARM
15:30 - 15:50	coffee break		
15:50 - 16:20	Mike Jacob	Signal integrity analysis in a cell based SOI design	Cadence
16:20 - 16:50	Phil Watson	Design methodology and tools for SOI based products	Synopsys
16:50 - 17:00	Horacio Mendez	Closing remarks	SOI Consortium