



LEADING DESIGN HOUSE JOINS THE SOI INDUSTRY CONSORTIUM

Time To Market Inc. Well-Positioned to Leverage SOI for Low-Power Applications, Including in India's Chip Design & Services Sector

BOSTON, MASS., December 11, 2008 – The SOI Industry Consortium, aimed at accelerating silicon-on-insulator (SOI) innovation into broad markets, announced today that Time To Market (TTM) Inc., an ASIC design and embedded software services company based in San Jose, California has joined the SOI Industry Consortium. TTM is a fully owned subsidiary of Infotech Enterprises Ltd (IEL), a global technology solutions provider headquartered in India. TTM has completed over 125 ASIC projects for leading electronics companies in the consumer, networking, and communications markets, and is well positioned to leverage SOI for low-power applications across all markets, especially in India's chip design and services sector.

“System-on-chips are both shrinking and getting denser each year, making the power/performance trade-off ever more critical,” says Bhanuprakash Cherukuri, TTM's Senior Vice President for Strategy and Head of High Tech Vertical. “SOI can provide lower power for the same performance, making it a very important technology. We look forward to working with the SOI Industry Consortium, helping to bring this technology into the mainstream and making it accessible to our customers.”

“We are very pleased to welcome Time To Market as a new member of the SOI Industry Consortium, complementing our focus on SOI design,” says Horacio Mendez, executive director of the SOI Industry Consortium. “This addition is key to the overall strategy of the consortium to expand SOI design capabilities. Time To Market's excellent engineering talents will also play a critical role in educating the market and expanding SOI's low-power capabilities in India.”

The SOI Industry Consortium is open to any company, organization or academic institution with an interest in SOI.

About the SOI Industry Consortium:

The SOI Industry Consortium is chartered with accelerating silicon-on-insulator (SOI) innovation into broad markets by promoting the benefits of SOI technology and reducing the barriers to adoption. Representing leaders spanning the entire electronics industry infrastructure, SOI Industry Consortium members include today: AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Nvidia, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Symmid Semiconductor, Synopsys, Time To Market, TSMC, Tyndall Institute, University Catholique de Louvain, and UMC. Membership is open to all companies and institutions throughout the electronics industry. For more information, see www.soiconsortium.org

Legal Note

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